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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/715,390	11/19/2003	Shigeo Tanaka	032116	5463
38834 7590 06/28/2007 WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP			EXAMINER	
1250 CONNECTICUT AVENUE, NW SUITE 700 WASHINGTON, DC 20036			BERHANE, ADOLF D	
			ART UNIT	PAPER NUMBER
	,		2838	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

			16
	Application No.	Applicant(s)	
	10/715,390	TANAKA ET AL.	
Office Action Summary	Examiner	Art Unit	
	Adolf Berhane	2838	
The MAILING DATE of this communication Period for Reply	appears on the cover sheet w	ith the correspondence address	
A SHORTENED STATUTORY PERIOD FOR REWHICHEVER IS LONGER, FROM THE MAILING - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication - If NO period for reply is specified above, the maximum statutory pe - Failure to reply within the set or extended period for reply will, by st Any reply received by the Office later than three months after the mearned patent term adjustment. See 37 CFR 1.704(b).	G DATE OF THIS COMMUNION R 1.136(a). In no event, however, may a lower in the community of	CATION. reply be timely filed ITHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on 3	0 April 2007.		
2a)⊠ This action is FINAL . 2b)□	This action is non-final.		
3) Since this application is in condition for allo	owance except for formal mat	ers, prosecution as to the merits is	
closed in accordance with the practice und	er <i>Ex parte Quayle</i> , 1935 C.D). 11, 453 O.G. 2 <u>1</u> 3.	
Disposition of Claims			
4)⊠ Claim(s) <u>1-15</u> is/are pending in the applica	tion.		
4a) Of the above claim(s) is/are with	drawn from consideration.		
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-4 and 12-15</u> is/are rejected.			
7)⊠ Claim(s) <u>5-11</u> is/are objected to.			
8) Claim(s) are subject to restriction ar	nd/or election requirement.		
Application Papers			
9)☐ The specification is objected to by the Exan	niner.		
10) The drawing(s) filed on is/are: a)	accepted or b) objected to	by the Examiner.	
Applicant may not request that any objection to	the drawing(s) be held in abeyar	nce. See 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the co	rrection is required if the drawing	(s) is objected to. See 37 CFR 1.121(d)).
11)☐ The oath or declaration is objected to by the	e Examiner. Note the attache	d Office Action or form PTO-152.	
Priority under 35 U.S.C. § 119			
12) ☐ Acknowledgment is made of a claim for fore a) ☐ All b) ☐ Some * c) ☐ None of:	eign priority under 35 U.S.C. {	} 119(a)-(d) or (f).	
 Certified copies of the priority document 	nents have been received.		
Certified copies of the priority document	nents have been received in A	application No	
3. Copies of the certified copies of the	•	received in this National Stage	
application from the International Bu	, , , , , , , , , , , , , , , , , , , ,		
* See the attached detailed Office action for a	list of the certified copies not	received.	
•			
Attachment(s)		·	
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) 		Summary (PTO-413) s)/Mail Date	
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date 	5) Notice of I 6) Other:	nformal Patent Application	
apor recommuni Dato	٠, ـ ٥,١٥٠٠		

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-4, 12 and 13 rejected under 35 U.S.C. 102(b) as being anticipated by Seiji Kono et al. (JP02000308257A).

Seiji Kono et al. disclose a power supply in Figs. 1 and 2. The output of and oscillation circuit, configured with a transistor TR1, resistors R1, R2, a capacitor C4 and a transformer TR, is smoothed with a diode D1 to provide a DC voltage from an output terminal Vout. In a waiting condition, a current detection circuit 22 detects the voltage drop of the resistor R4, sets a latching circuit 21, makes the transistor TR2 conductive and stops oscillation to stop an output from the output terminal Vout. In this case, when a drive signal turns on a switch SW, a Zener diode ZD2 is turned on, a current flows into a photocoupler 4, the latching circuit 21 is reset, the transistor TR2 is turned off, the oscillation circuit starts the oscillation, and thereby a DC voltage can be obtained from the output terminal Vout.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

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4. Claims 14 and 15 are rejected under 35 U.S.C. 102(a) as being anticipated by Bruning (6,339,314).

Bruning disclose a battery charger with low standby power dissipation in Figs. 1 and 2. Stopping a power supply to the transformer (30) when an output side of the current-to-voltage conversion circuit (10) in a no-load state (see col. 1, lines 62-66) and starting a power supply to the transformer when an external voltage is applied to the output side of the current-to-voltage conversion circuit (col. 1, line 67 to col. 2, line 2) wherein the external voltage is applied to the output side of the current-to-voltage conversion circuit in a deactivated state, by controlling ON and OFF states of a switching circuit (22) within an electronic apparatus to which the current-to-voltage conversion circuit is coupled an input section to input an input power (12, 14) a transformer to convert the input power into an output power (16, 18) an output section to output the output power, sensing circuit (104), threshold detector (112) and timing circuit (114).

Allowable Subject Matter

5. Claims 5-11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Response to Arguments

6. Applicant's arguments filed 4/30/07 have been fully considered but they are not persuasive. In response to Applicant argument with respect to claim 14 and 15 recited that a no-load or standby state is detected by detecting a state of the output side of the

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current to voltage conversion circuit and further try to claim by example that in accordance with the present invention the no-load or standby state is detected by detecting the current in the secondary side circuit of the transformer (i.e., the output side), applicant misinterprets the principle that claims are interpreted in the light of the specification. Although these limitation "the detecting the current in the secondary side circuit of the transformer" are found as examples or embodiments in the specification, they were not claimed explicitly. Nor were the words that are used in the claims defined in the specification to require these limitations. A reading of the specification provides no evidence to indicate that these limitations must be imported into the claims to give meaning to disputed terms. Constant V. Advanced Micro-Devices Inc., 7 USPQ2d 1064. Bruning disclose or suggest a starting a power supply to the transformer when an external voltage is applied to the output side of the current-to-voltage conversion circuit, see col. 1, line 67 to col. 2, line 2, a threshold detector is coupled to the triggerable electronic switch trigger the triggerable electronic switch when the voltage of the threshold detector reaches a predetermined value (i.e. an external voltage is applied to the conversion circuit). Applicant further argues that Bruning does not disclose or suggest stopping a supply of power to the transformer when the output side of the current-to-voltage conversion circuit is the no load or light load state. Applicant's attention is directed to col. 1, lines 62-67 which state that the when there is no load the power supply is shut off (stopped).

7. Applicant's arguments with respect to claims 1-4, 12, and 13 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

9. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Adolf Berhane whose telephone number is 571-272-2077. The examiner can normally be reached on Monday- Friday 8 AM to 6 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Karl Easthom can be reached on 571-272-1989. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Adolf Berlfane Primary Examiner Art Unit 2838